

2836

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. RGP-0046	
In Re Application Of: CONNELLY et al.				
Serial No. 09/878,127	Filing Date June 8, 2001	Examiner	Group Art Unit 2835	
Title: METHOD OF MANUFACTURING CIRCUIT LAMINATES				
Address to: Assistant Commissioner for Patents Washington, D.C. 20231				
<p style="text-align: center;">37 CFR 1.97(b)</p> <p>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p style="text-align: center;">37 CFR 1.97(c)</p> <p>2. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <p><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</p> <p style="text-align: center;">OR</p> <p><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</p>				

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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
RGP-0046

In Re Application: CONNELLY et al.

Serial No.
09/878,127

Filing Date
June 8, 2001

Examiner

Group Art Unit
2835

METHOD OF MANUFACTURING CIRCUIT LAMINATE

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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Applicant(s): CONNELLY et al.

Docket No.

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Group Art Unit

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Invention: METHOD OF MANUFACTURING CIRCUIT LAMINATES

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